

FIG.1

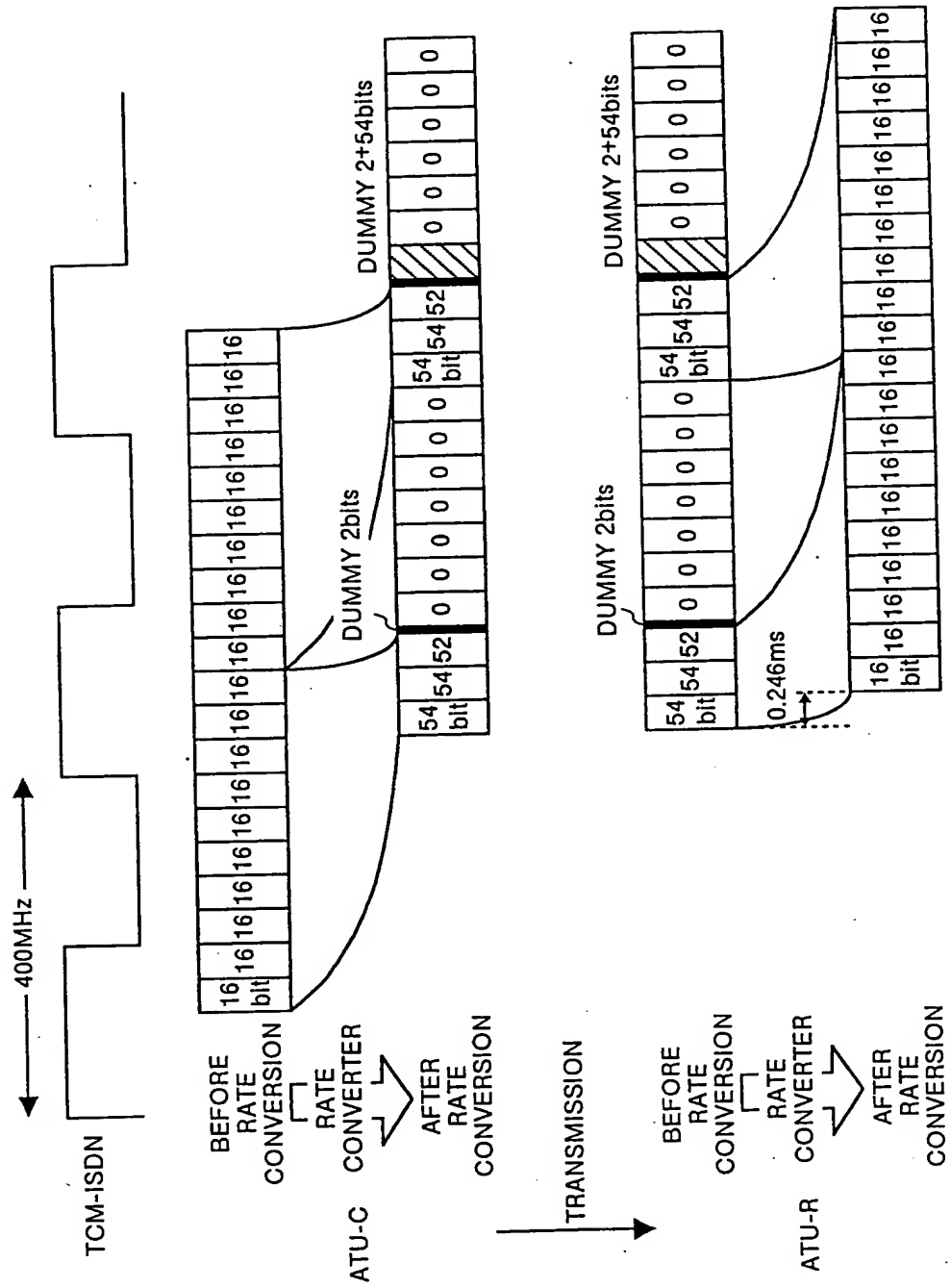


FIG.2

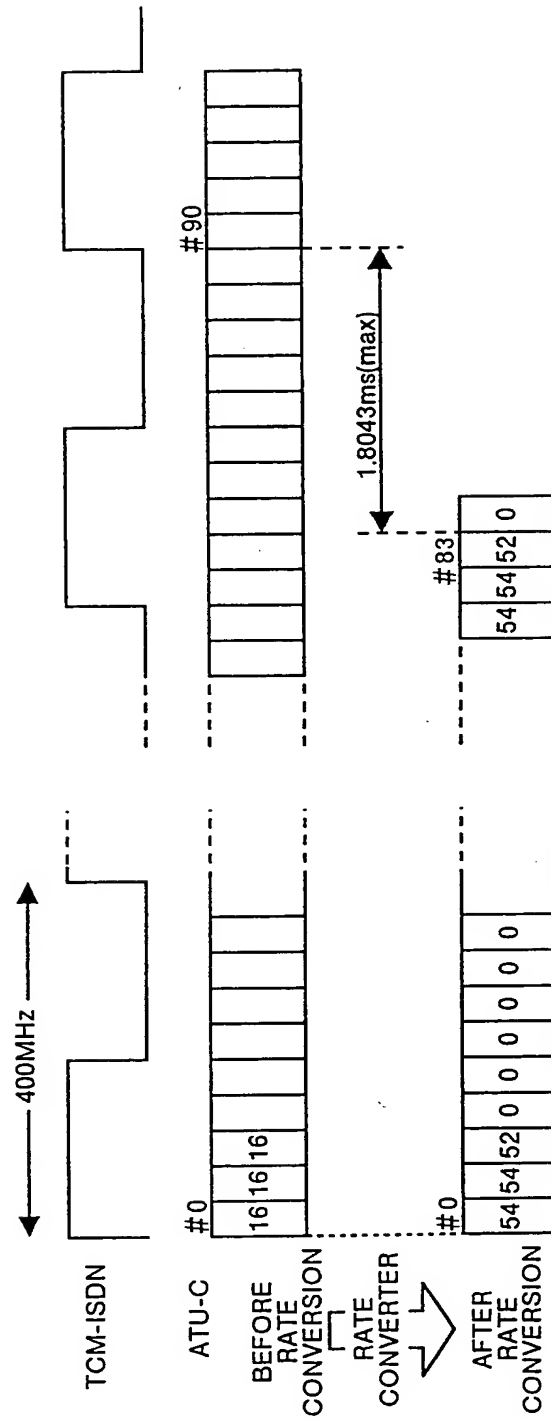


FIG. 4

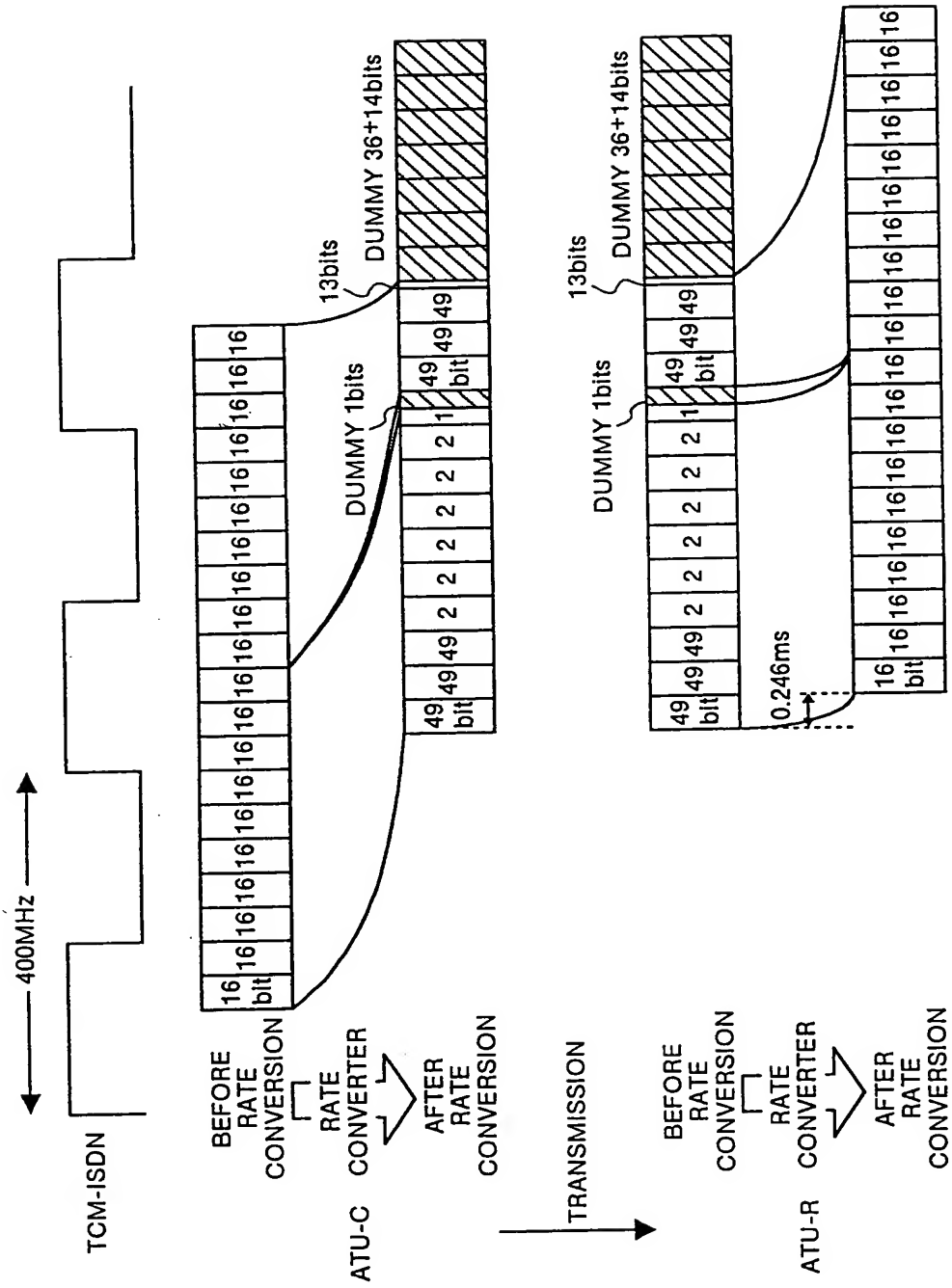


FIG. 5

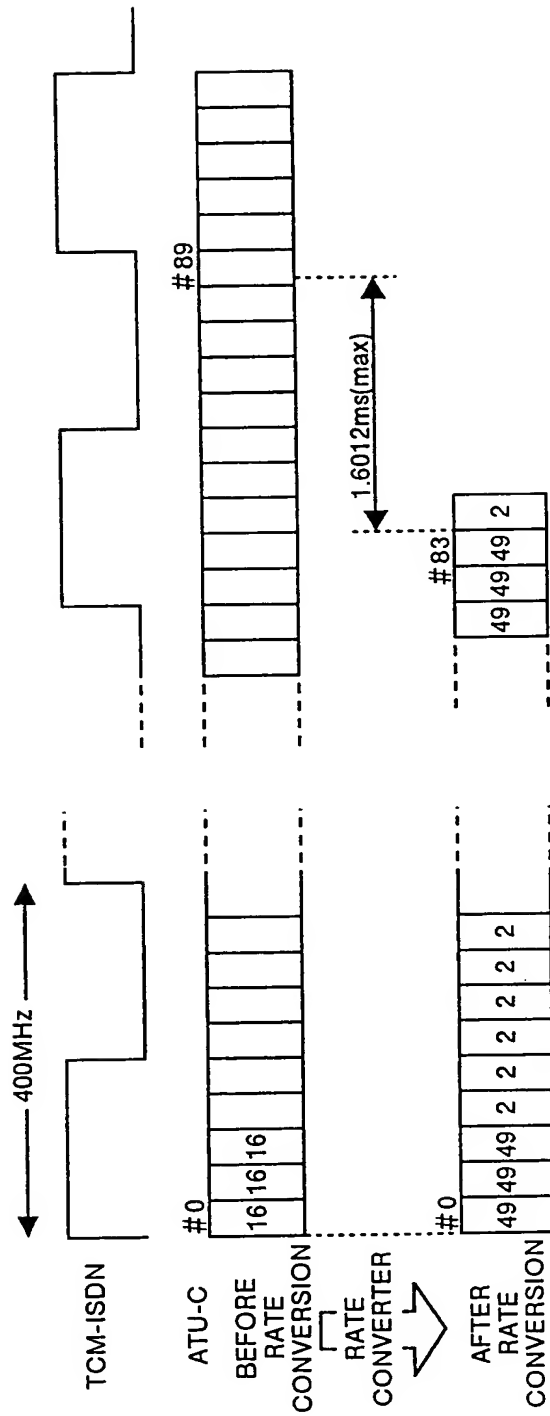


FIG.7

m_i	Parameter
$m_{47} - m_{44}$	Minimum required SNR margin
$m_{43} - m_{18}$	Reserved for future use
m_{17}	trellis coding option
m_{16}	echo cancelling option
m_{15}	unused (shall be set to 1)
m_{14}	Bitmap B mode
$m_{13} - m_{12}$	Reserved for future use
m_{11}	NTR
$m_{10} - m_9$	Framing mode
$m_8 - m_6$	Transmit PSD during initialization
$m_5 - m_4$	Reserved
$m_3 - m_0$	Maximum numbers of bits per sub-carrier supported

FIG.8

m_i	Parameter
$m_{47} - m_{44}$	Minimum required SNR margin
$m_{43} - m_{18}$	Reserved for future use
m_{17}	trellis coding option
m_{16}	echo cancelling option
m_{15}	unused (shall be set to 1)
m_{14}	Bitmap B mode
$m_{13} - m_{12}$	Low payload transfer delay mode
m_{11}	NTR
$m_{10} - m_9$	Framing mode
$m_8 - m_6$	Transmit PSD during initialization
$m_5 - m_4$	Reserved
$m_3 - m_0$	Maximum numbers of bits per sub-carrier supported

FIG.9

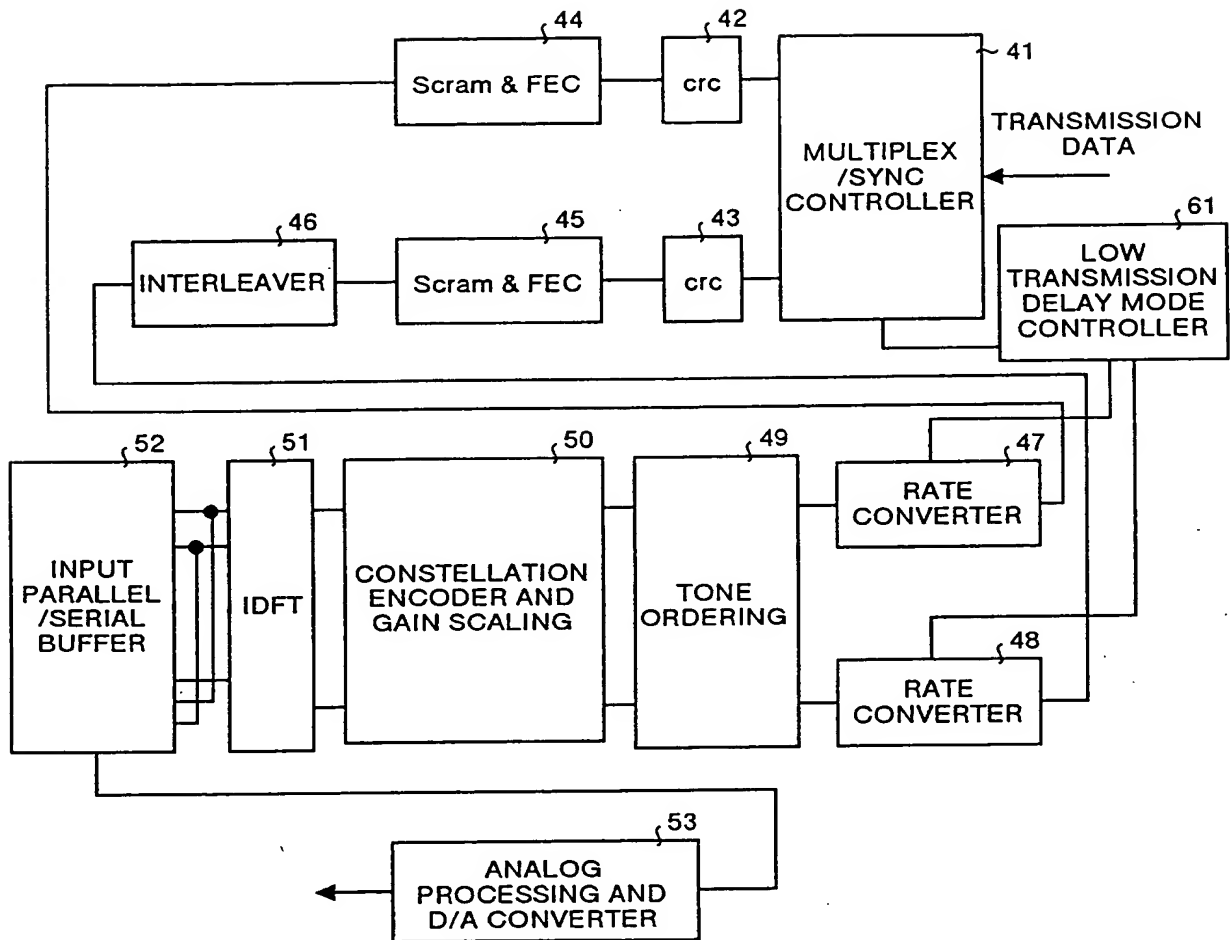


FIG.10

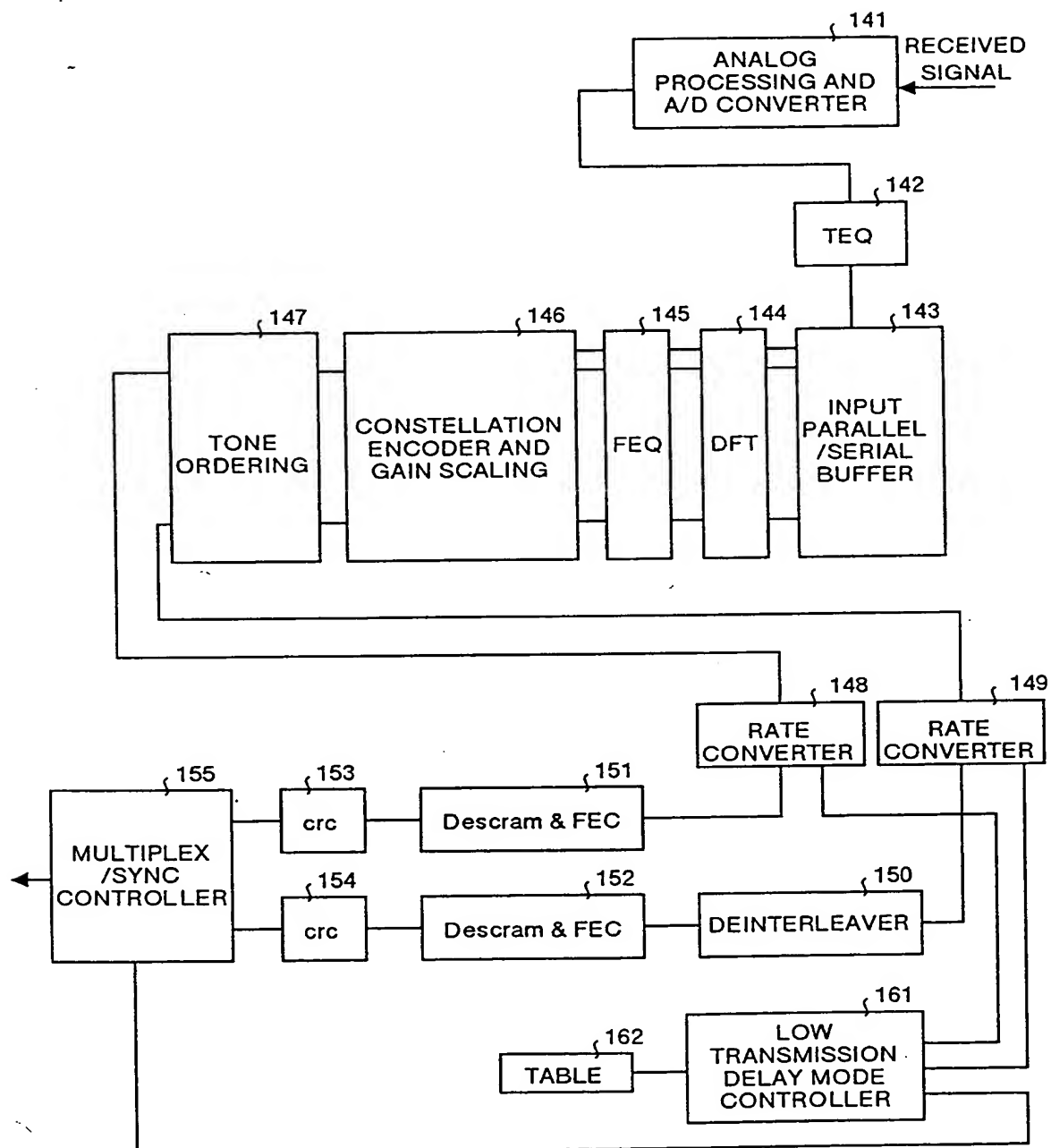
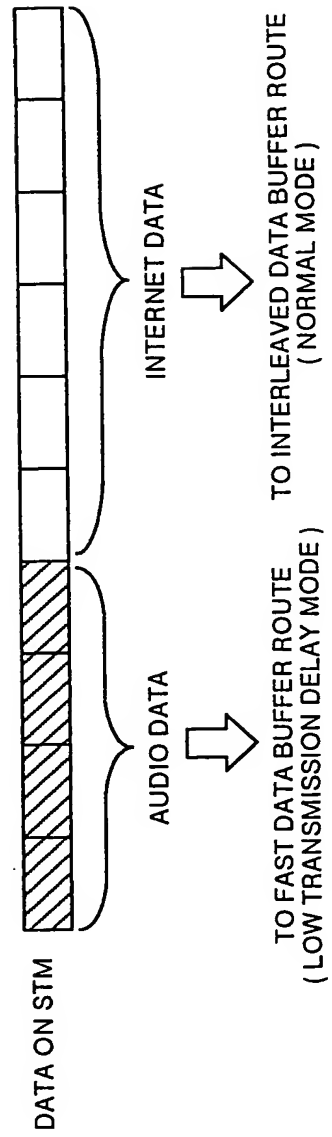


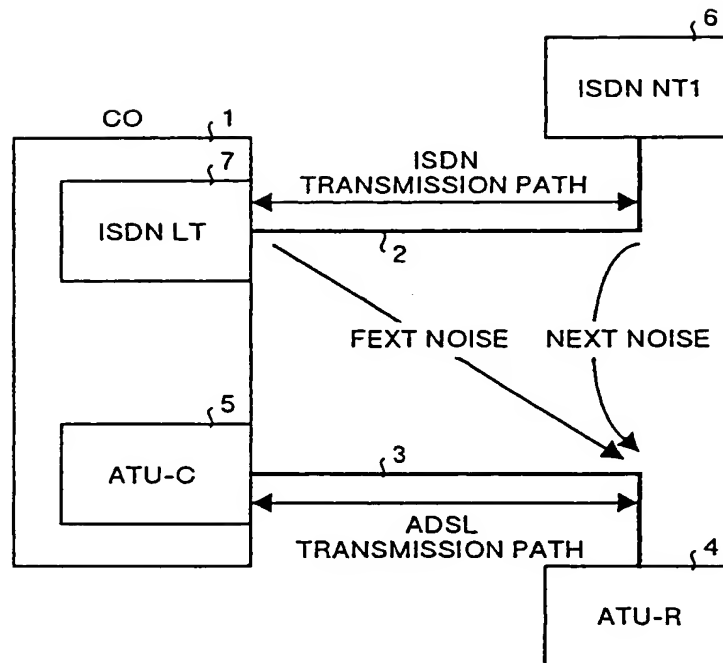
FIG.11



REPLACEMENT SHEET

12/22

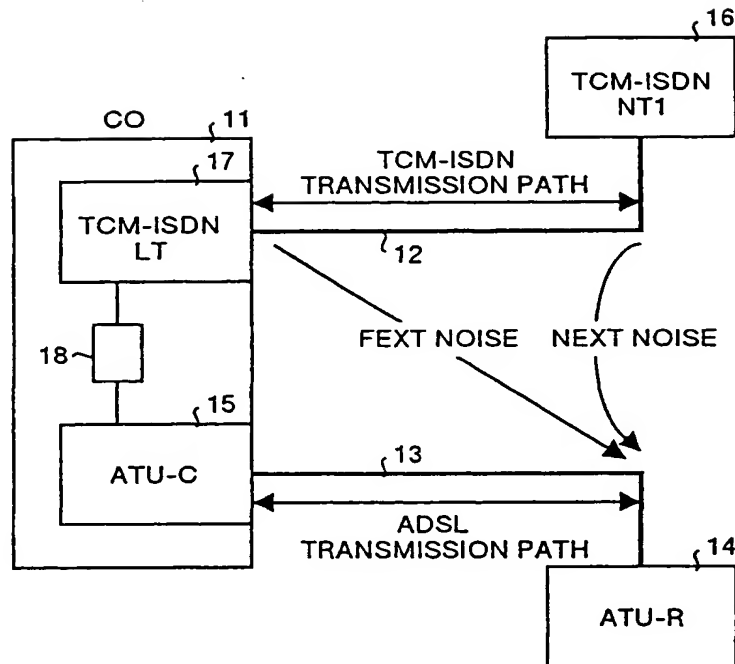
FIG.12
CONVENTIONAL ART



REPLACEMENT SHEET

13/22

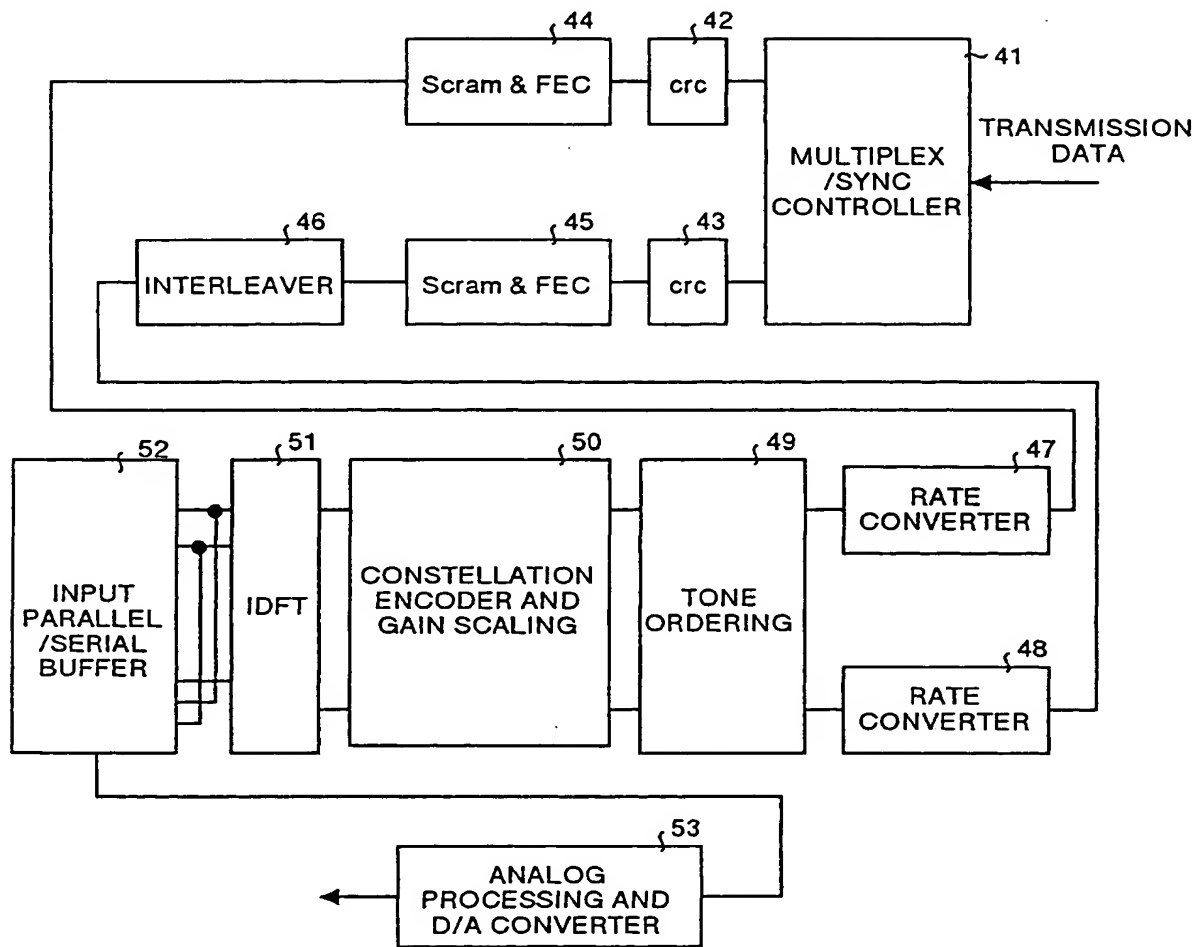
FIG.13
CONVENTIONAL ART



REPLACEMENT SHEET

14/22

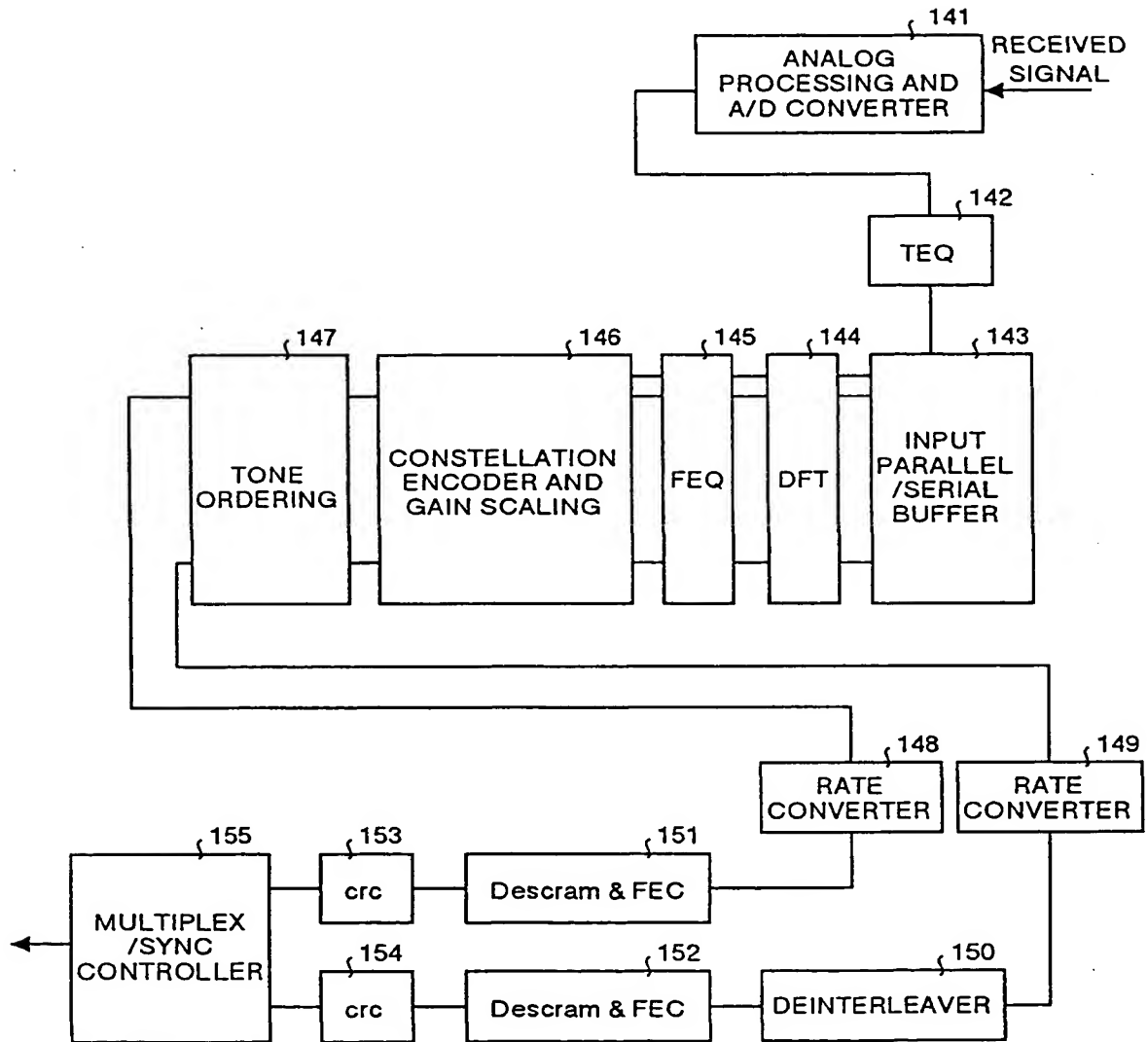
FIG.14
CONVENTIONAL ART



REPLACEMENT SHEET

15/22

FIG.15
CONVENTIONAL ART



16/22

FIG. 16
CONVENTIONAL ART

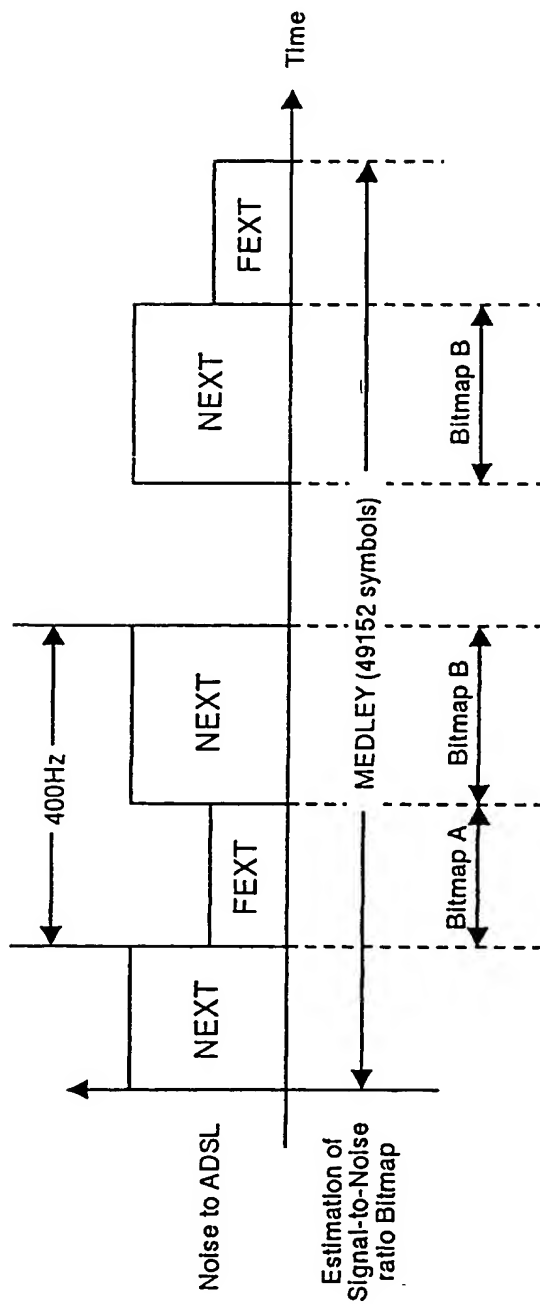
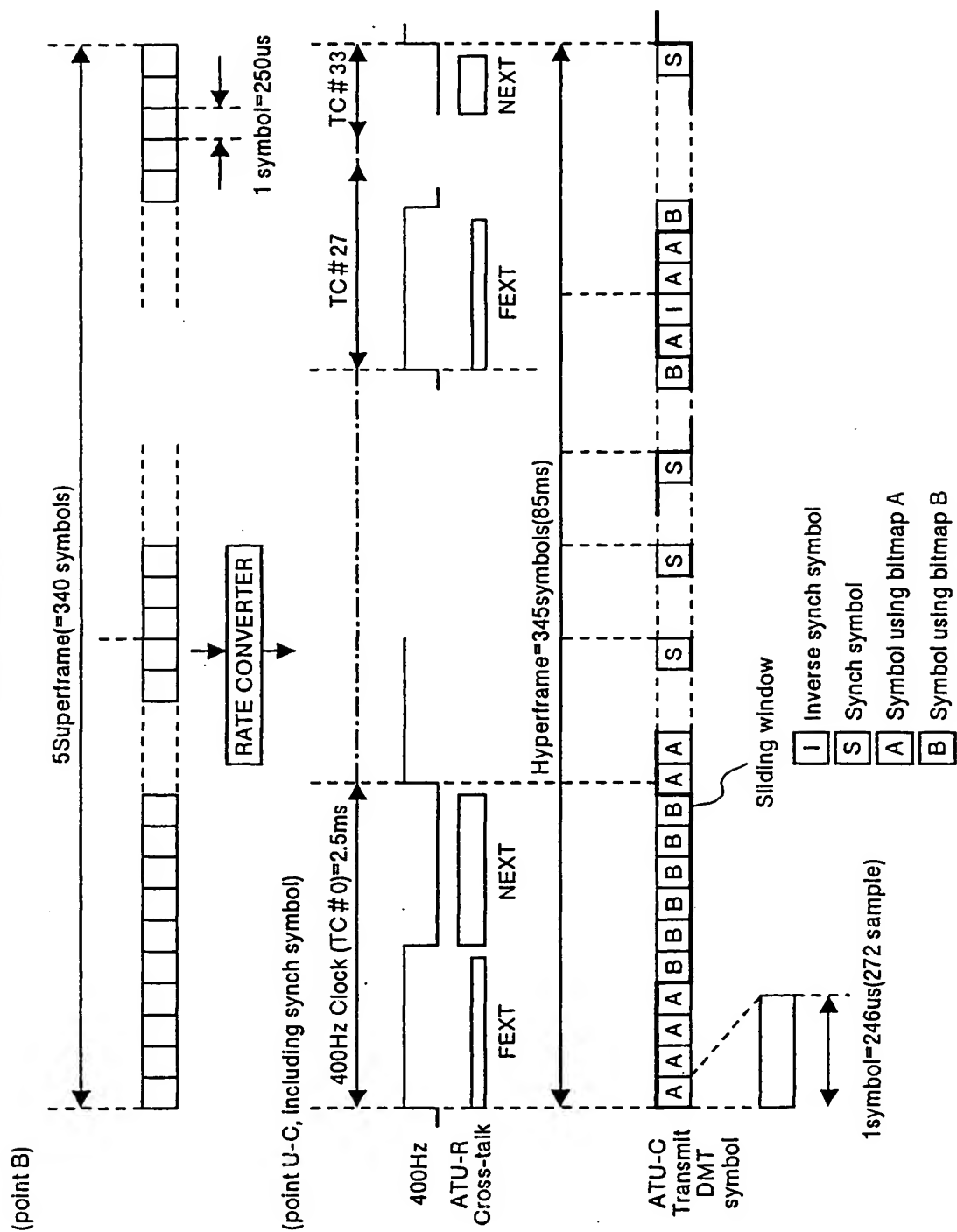


FIG. 17
CONVENTIONAL ART



REPLACEMENT SHEET

18/22

FIG.18
CONVENTIONAL ART

400Mz

	FEXT SECTION										NEXT SECTION									
0	0	1	2	3	4	5	6	7	8	9										
1	10	11	12	13	14	15	16	17	18	19										
2	20	21	22	23	24	25	26	27	28	29										
3	30	31	32	33	34	35	36	37	38	39	40									
4	41	42	43	44	45	46	47	48	49	50										
5	51	52	53	54	55	56	57	58	59	60										
6	61	62	63	64	65	66	67	SS	69	70										
7	71	72	73	74	75	76	77	78	79	80										
8	81	82	83	84	85	86	87	88	89	90										
9	91	92	93	94	95	96	97	98	99	100										
10	101	102	103	104	105	106	107	108	109	110	111									
11	112	113	114	115	116	117	118	119	120	121										
12	122	123	124	125	126	127	128	129	130	131										
13	132	133	134	135	136	SS	138	139	140	141										
14	142	143	144	145	146	147	148	149	150	151										
15	152	153	154	155	156	157	158	159	160	161										
16	162	163	164	165	166	167	168	169	170	171	172									
17	173	174	175	176	177	178	179	180	181	182										
18	183	184	185	186	187	188	189	190	191	192										
19	193	194	195	196	197	198	199	200	201	202										
20	203	204	205	SS	207	208	209	210	211	212										
21	213	214	215	216	217	218	219	220	221	222										
22	223	224	225	226	227	228	229	230	231	232										
23	233	234	235	236	237	238	239	240	241	242	243									
24	244	245	246	247	248	249	250	251	252	253										
25	254	255	256	257	258	259	260	260	262	263										
26	264	265	266	267	268	269	270	271	272	273										
27	274	SS	276	277	278	279	280	281	282	283										
28	284	285	286	287	288	289	290	291	292	293										
29	294	295	296	297	298	299	300	301	302	303										
30	304	305	306	307	308	309	310	311	312	313	314									
31	315	316	317	318	319	320	321	322	323	324										
32	325	326	327	328	329	330	331	332	333	334										
33	335	336	337	338	339	340	341	342	343	SS										

ISS Inverse synch symbol
 Symbol using bitmap A

SS Synch symbol
 Symbol using bitmap B

FIG.19
CONVENTIONAL ART

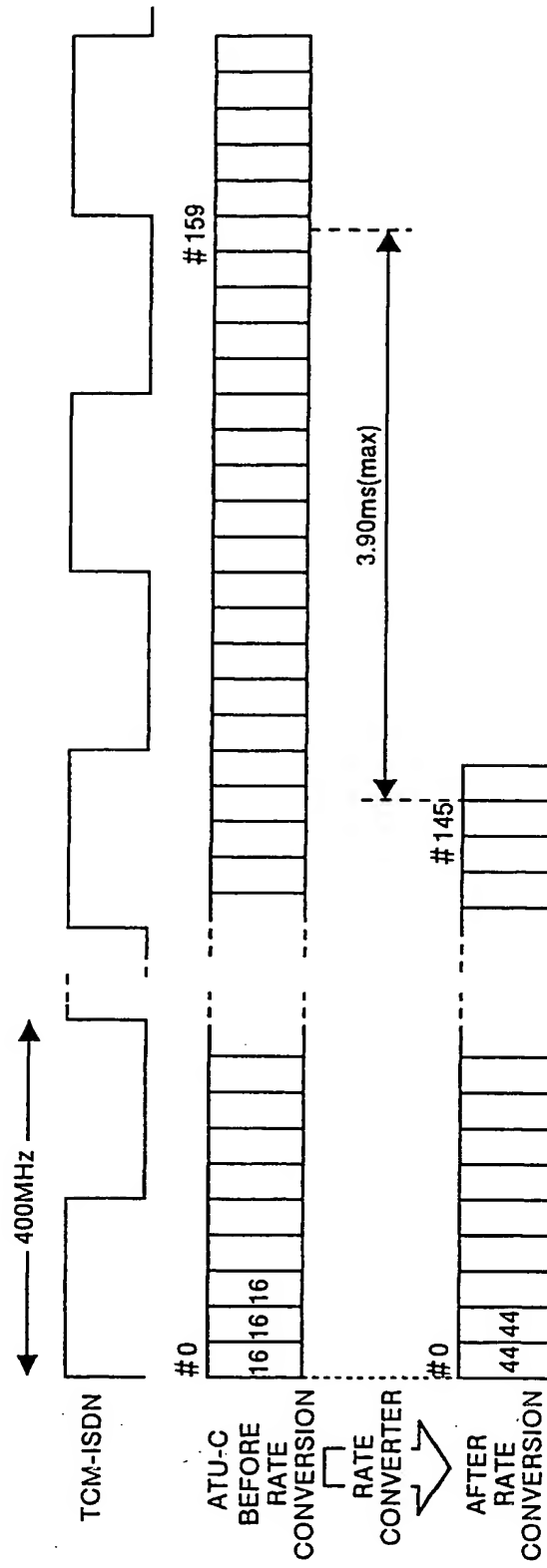


FIG.20
CONVENTIONAL ART

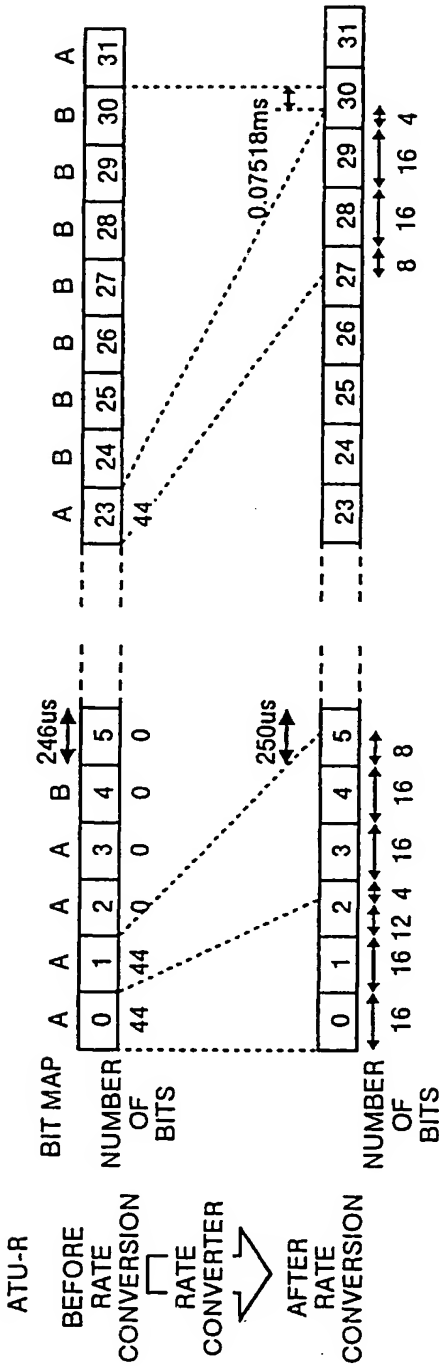
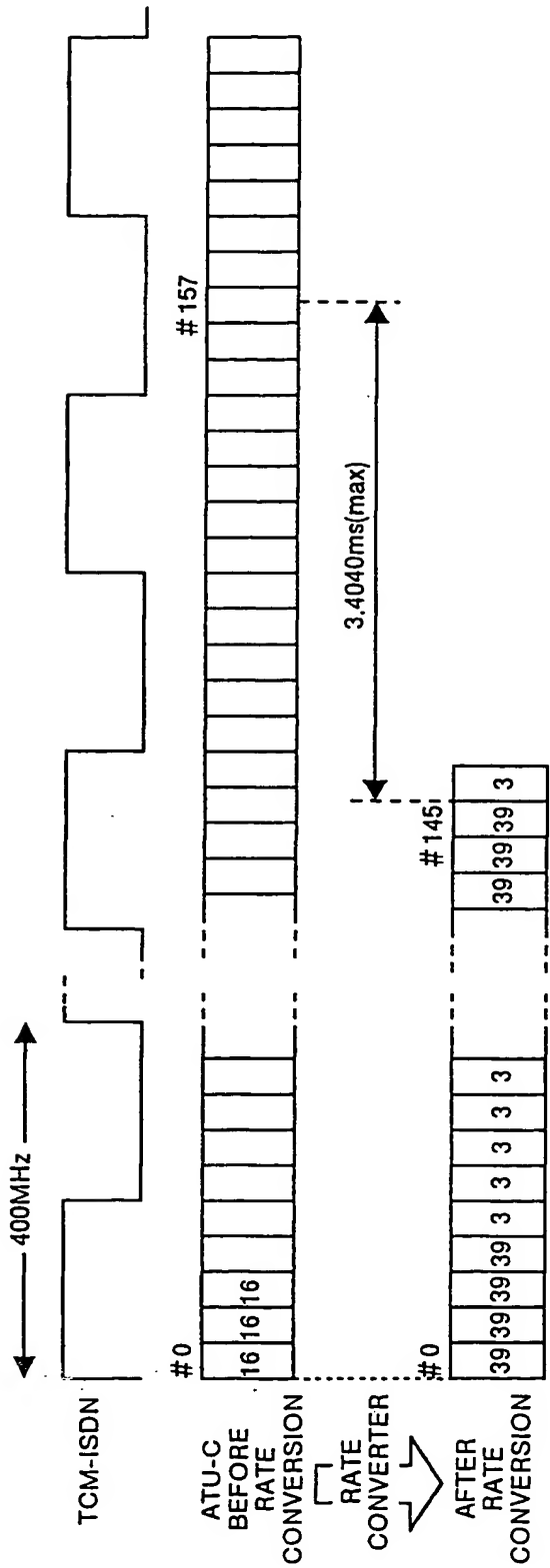


FIG.21
CONVENTIONAL ART



22/22

FIG.22
CONVENTIONAL ART

